

LISTING OF THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. **(Currently Amended)** A bandgap voltage reference circuit comprising:
a first circuit providing a first voltage representative of ~~substantially proportional to~~ V_{be} of a first bipolar transistor;
a second circuit providing a second voltage ΔV_{be} representative of ~~substantially proportional to~~ the difference of two V_{be} voltages of two additional bipolar transistors; and
a comparator having respective inputs receiving voltages representative of ~~coupled to~~ V_{be} and ΔV_{be} and an output coupled to the base of the first bipolar transistor whereby a voltage representative of ~~substantially proportional to~~ the sum of respective constants multiplying V_{be} and ΔV_{be} is provided at the output of the comparator.
2. **(Currently Amended)** A bandgap voltage reference circuit comprising:
a first bipolar transistor providing substantially a reference voltage V_{be} ;
a current mirror circuit comprising two bipolar transistors coupled in a current mirror arrangement for providing a voltage difference ΔV_{be} comprising substantially a difference signal between the respective V_{be} voltages of the two bipolar transistors; and
a comparator having respective inputs receiving voltages representative of ~~coupled to~~ V_{be} and ΔV_{be} and an output coupled to the base of the first bipolar transistor whereby a voltage representative of ~~substantially proportional to~~ the sum of respective constants multiplying V_{be} and ΔV_{be} is provided at the output of the comparator.
3. **(Currently Amended)** A bandgap voltage reference circuit comprising:
a first circuit providing a first voltage representative of ~~substantially proportional to~~ V_{be} of a first bipolar transistor;
a second circuit providing a second voltage ΔV_{be} representative of ~~substantially proportional to~~ the difference of two V_{be} voltages of two additional bipolar transistors; and

a comparator having respective inputs receiving voltages representative of ~~coupled to~~ V_{be} and ΔV_{be} and an output coupled to the base of the first bipolar transistor whereby a substantially temperature independent voltage reference ~~reference~~ is provided at the output of the comparator.